

Dr. Nizar Dahir



Iraqi Cultural Attache, Beijing, China

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Highlights

- More than 17 years' experience in academic teaching, research, and higher education administration
- **Leading researcher** for **EPSRC/UK** funded research on adaptive Many-Core Systems;
- **Fulbright Visiting Scholar** at the University of Delaware, **Newark, USA**;
- About **15 years' research and teaching experience** in Computer Engineering and Microelectronics.
- IET Computers & Digital Techniques Journal **Premium Best Paper Award**.
- **Best poster award**, Design Automation Conference **DAC'16, USA**.
- Best paper award, Cultural Attaché Conference on Engineering Sciences, **London United Kingdom**.

Education

Newcastle University, Newcastle, United Kingdom

- **PhD.**, in Computer Engineering (Sep 2010 – Mar 2015)

Al-Nahrain University, Baghdad, Iraq

- **M.Sc. Computer Engineering (Sep 1997 – Sep 1999)**

Al-Nahrain University, Baghdad, Iraq

- **B.Sc. Computer Engineering (Sep 1994 – Sep 1997)**

Employment

Academic Attaché, Iraqi Cultural Attaché in Beijing

Feb. 2022 - Present

- Main duties include following up with academic progress of Iraq students in China as well as coordinating research and academic collaboration opportunities for the Iraqi higher education sector with Chinese HE institutions and managing joint projects and funding with Chinese universities and organisations.

Head of Department, Ministry of Higher Education and Scientific Research HQ in Baghdad, Iraq

Sept. 2019 – Feb. 2022

- **Head of International Collaboration Department** at the directorate of scholarships and cultural relations in the ministry responsible for international research and academic collaboration opportunities for the Iraqi higher education sector and for monitoring and overseeing joint projects and funding with foreign universities and organisations.
- Part-time lecturer at the College of Information Engineering, and supervising post-graduate students, Al-Nahrain University - Baghdad

Lecturer at the University of Kufa, Iraq

April 2017 – Sept. 2019

- **Deputy Director** of IT Research and Development Centre responsible for research management and **research strategy planning**. Main research areas were Embedded Systems, Reconfigurable Computing and FPGAs, Machine Learning.
- Lecturer at the department of Electronic Engineering to deliver lectures in Computer Architecture and HDL Lab.

Fulbright Visiting Scholar at the University of Delaware, Newark, USA

June 2018 – September 2018

- Among the 23 who were accepted for the Fulbright Visiting Scholarship from around 600 applicants. The program is fully sponsored by the US department of state.

- Worked as researcher at the department of Electrical and Computer Engineering with the CMOS VLSI Opto-Electronics Research Group (CVORG).
- Participated in many workshops for developing research and teaching skills of faculty members.

Research Associate at the Department of Electronics, University of York, UK

January 2014 – April 2017 (3 years 4 months)

Working on a project under **EPSRC** grant **EP/L000563/1**: *Continuous on-line adaptation in many-core systems: From graceful degradation to graceful amelioration.*

My major duties are:

- Design, implement, and test a custom **FPGA-based embedded system** for building a many core (100+) research platform which consists of ARM cores and support runtime control of power and thermal and power sensing.
- Designing high-performance, energy efficient and **adaptive Network-on-Chip** as communication infrastructure for the target platform with interfaces to the ARM cores.
- Coordinate the requirements and contributions of the three institutions involved in the project (York, Manchester and Southampton) and integrate these within the system.
- The implementation of parallel application benchmarks and evolutionary algorithms on the many-core platform.
- Responsible for designing and **teaching** of the third year undergraduate course titled “Digital Engineering”.

Research Assistant at the Department of EEE, Newcastle University, UK

September 2010 – January 2014 (4 years 3 Months)

- Worked with the microelectronics research group. Research mainly focused on:
 - Researching the serious **thermal, power** and **performance** challenges for **3D IC** and **3D CMPs**.
 - Designing high-performance, physical parameter- aware, Networks-on-Chip (NoCs) for future 2D and 3D many-core systems.
 - Design and FPGA implementation of thermally adaptive routing for NoCs.
 - Experience gained in various electronics **EDA tools** and digital design and simulation languages including **SPICE, Synopsys Design Compiler, Verilog, SystemC** and **Matlab**.
- Teaching assistance for many postgraduate and undergraduate modules including; DSP, FPGAs, Microprocessors, Microcontrollers and C++.

Teaching Staff Member and Researcher at the University of Kufa, Iraq

December 2005 – August 2010 (4 years 8 months)

- Researcher in the University of Kufa - IT Research and Development Centre. Main research areas were Image Recognition and Embedded Systems.
- Assistant lecturer at the department of Computer Science and Electrical Engineering to deliver lectures in various subject in Computer Engineering including Computer Network, Image Processing, Digital Electronics and Web Programming. Moreover, I have **supervised more than 7 undergraduate projects** during this period.

Publications

More than 40 publications in top journals including IEEE Transactions, ACM transactions, IET and Elsevier as well as top conferences in the field such as DAC, DATE, CODES. Follow [this link](#) for full list of publications.

Administrative Duties

Technical Program Committee:

- International Conference on Evolvable Systems (ICES), 2016
- Symposium Series on Computational Intelligence (IEEE SSCI), 2016
- International workshop on Many-core Embedded Systems Finland 2015

Regular Reviewer: IEEE TCAD, IEEE TVLSI, IEEE TC, ACM TEC, IEEE TCAS, ACM TODAES, IET CDT, Elsevier Microelectronics.